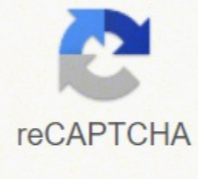


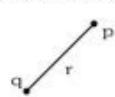


I'm not robot



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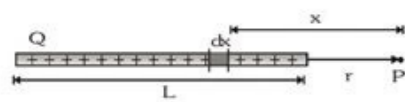
Electric Potential due to a point charge in its surrounding :



The potential at a point P at a distance r from the charge q,  $V_p = \frac{U}{q_0}$ . Where U is the potential energy of charge  $q_0$  at point p,  $U = \frac{kq_0q}{r}$ . Thus potential at point P is  $V_p = \frac{kq}{r}$ .

Electric Potential due to a charged Rod :

Figure shows a rod of length L, uniformly charged with a charge Q. Due to this we'll find electric potential at a point P at a distance r from one end of the rod as shown in figure.



For this we consider an element of width dx at a distance x from the point P.

Charge on this element is  $dQ = \frac{Q}{L} dx$ .

The potential dV due to this element at point P can be given by using the result of a point charge as

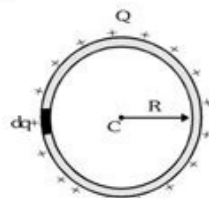
$$dV = \frac{k dQ}{x} = \frac{kQ}{L} \frac{dx}{x}$$

Net electric potential at point P :  $V = \int \frac{kQ}{L} \frac{dx}{x} = \frac{kQ}{L} \ln \left( \frac{r+L}{r} \right)$

Electric potential due to a charged ring

Case - 1 : At its centre

To find potential at the centre C of the ring, we first find potential dV at centre due to an elemental charge dq which is given as  $dV = \frac{k dq}{R}$ . Total potential at C is  $V = \int dV = \int \frac{k dq}{R} = \frac{kQ}{R}$ .



As all dq's of the ring are situated at same distance R from the ring centre C, simply the potential due to all dq's is added as being a scalar quantity, we can directly say that the total electric potential at ring centre is  $\frac{kQ}{R}$ . Here we can also state that even if charge Q is non-uniformly distributed on ring, the electric potential C will remain same.

**GOLDEN KEY POINTS**

- Charged particle in an electric field always experiences a force either it is at rest or in motion.
- In presence of a dielectric, electric field decreases and becomes  $\frac{1}{\epsilon_r}$  times of its value in free space.
- Test charge is always a unit (+ve) charge.  $E = \frac{F_{test}}{q_{test}}$
- If identical charges are placed on each vertex of a regular polygon, then  $E$  at centre = zero.

**ELECTRIC FIELD INTENSITIES DUE TO VARIOUS CHARGE DISTRIBUTIONS**

Due to discrete distribution of charge  
Field produced by a charge distribution for discrete distribution-

By principle of superposition intensity of electric field due to P charge  $E_p = \frac{kq}{r^2}$ .  
∴ Net electric field due to whole distribution of charge  $E_p = \sum_{i=1}^n E_i$

Continuous distribution of charge  
Treating a small element as particle  $E = \frac{1}{4\pi\epsilon_0} \frac{dq}{r^2}$

Due to linear charge distribution  $E = k \int \frac{\lambda dl}{r^2}$  ( $\lambda$  = charge per unit length)

Due to surface charge distribution  $E = k \int \frac{\sigma da}{r^2}$  ( $\sigma$  = charge per unit area)

Due to volume charge distribution  $E = k \int \frac{\rho dv}{r^2}$  ( $\rho$  = charge per unit volume)

Electric field strength at a general point due to a uniformly charged rod

As shown in figure, if P is any general point in the surrounding of rod, to find electric field strength at P, we consider an element of rod of length dx at a distance x from point O as shown in figure. Let dE be the electric field at P due to the element, then

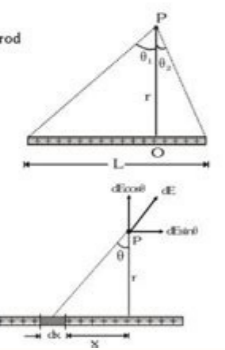
$$dE = \frac{k dq}{(x^2 + r^2)} \text{ Here } dq = \frac{Q}{L} dx$$

Electric field strength in x-direction due to dq at P is

$$dE_x = dE \cos \theta = \left[ \frac{k \lambda dx}{(x^2 + r^2)} \right] \frac{x}{\sqrt{x^2 + r^2}}$$

Here we have  $x = r \tan \theta$  and  $dx = r \sec^2 \theta d\theta$

$$\text{Then } dE_x = \frac{k \lambda}{r} \frac{r \sec^2 \theta d\theta}{\sqrt{r^2 \sec^2 \theta}} \cos \theta = \frac{k \lambda}{r} \sin \theta d\theta$$



14.14. In a p-n junction diode, the current I can be expressed as

$$I = I_0 \exp \left( \frac{eV}{k_B T} - 1 \right)$$

where  $I_0$  is called the reverse saturation current, V is the voltage across the diode and is positive for forward bias and negative for reverse bias, and I is the current through the diode,  $k_B$  is the Boltzmann constant ( $8.6 \times 10^{-5}$  eV/K) and T is the absolute temperature. If for a given diode  $I_0 = 5 \times 10^{-12}$  A and  $T = 300$  K, then

- What will be the forward current at a forward voltage of 0.6 V?
- What will be the increase in the current if the voltage across the diode is increased to 0.7 V?
- What is the dynamic resistance?
- What will be the current if reverse bias voltage changes from 1 V to 2 V?

**Solution:**

Given:

The current I in a p-n junction diode,  $I = I_0 \exp \left( \frac{eV}{k_B T} - 1 \right)$

$$I_0 = 5 \times 10^{-12} \text{ A}$$

$$T = 300 \text{ K}$$

$$k_B = 8.6 \times 10^{-5} \text{ eV/K}$$

(a) Forward voltage 0.6 V

$$I = 5 \times 10^{-12} \exp \left( \frac{0.6}{8.6 \times 10^{-5} \times 300} - 1 \right)$$

$$\Rightarrow I = 5 \times 10^{-12} \exp(22.25) = 0.0231 \text{ A}$$

The forward current in the diode is 0.0231 A.

(b) Forward voltage 0.7 V

$$I = 5 \times 10^{-12} \exp \left( \frac{0.7}{8.6 \times 10^{-5} \times 300} - 1 \right)$$

$$\Rightarrow I = 5 \times 10^{-12} \exp(26.13) = 1.116 \text{ A}$$

The forward current in the diode is 1.116 A.

(c) Dynamic Resistance is given as the ratio of change in voltage and change in current.

$$\text{Dynamic Resistance} = \frac{\text{change in voltage}}{\text{change in current}} = \frac{0.7 - 0.6}{1.116 - 0.0231} = \frac{0.1}{1.093} = 0.091 \Omega$$

**Specific use of conducting materials :**

- The **heating element** of devices like heater, geyser, press etc are made of **microhm** because it has high resistivity and high melting point. It does not react with air and acquires steady state when red hot at 800 C.
- **Fuse wire** is made of **tin lead alloy** because it has low melting point and low resistivity. The fuse is used in series, and melts to produce open circuit when current exceeds the safety limit.
- **Resistances** of resistance box are made of **manganin** or **constantan** because they have moderate resistivity and very small temperature coefficient of resistance. The resistivity is nearly independent of temperature.
- The **filament of bulb** is made up of **tungsten** because it has low resistivity, high melting point of 3300 K and gives light at 2400 K. The bulb is filled with inert gas because at high temperature it reacts with air forming oxide.
- The **connection wires** are made of **copper** because it has low resistance and resistivity.

**COLOUR CODE FOR CARBON RESISTORS**

Colour	Strip A	Strip B	Strip C	Strip D (Tolerance)
Black	0	0	10 <sup>0</sup>	
Brown	1	1	10 <sup>1</sup>	
Red	2	2	10 <sup>2</sup>	
Orange	3	3	10 <sup>3</sup>	
Yellow	4	4	10 <sup>4</sup>	
Green	5	5	10 <sup>5</sup>	
Blue	6	6	10 <sup>6</sup>	
Violet	7	7	10 <sup>7</sup>	
Grey	8	8	10 <sup>8</sup>	
White	9	9	10 <sup>9</sup>	
Gold	-	-	10 <sup>-1</sup>	± 5 %
Silver	-	-	10 <sup>-2</sup>	± 10 %
No colour	-	-	-	± 20 %

May be remembered as  
**BBROY**  
 Great Britain  
 Very Good Wife.

**Example**

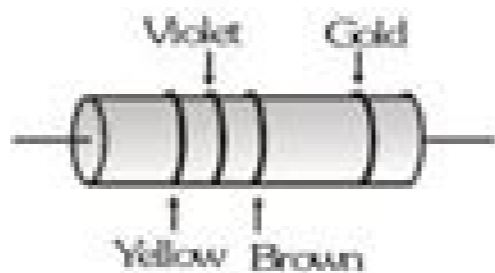
Draw a colour code for 42 k Ω ± 10% carbon resistance.

**Solution**

According to colour code colour for digit 4 is yellow, for digit 2 it is red, for 3 colour is orange and 10% tolerance is represented by silver colour. So colour code should be yellow, red, orange and silver.

**Example**

What is resistance of following resistor.



Number for yellow is 4, Number of violet is 7

Brown colour gives multiplier 10<sup>1</sup>, Gold gives a tolerance of ± 5%

So resistance of resistor is 47 10<sup>1</sup> Ω ± 5% = 470 ± 5% Ω.

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enozatneserppar aL.1 otats ol onognuiggar tupni 'Aip o onu es 1 otats ol egnuiggar RO etag led tuptuo'L.setaG RONXE e ROXE ,RON ,DNAN emoc illecnac irtla omainetto idniUQ .4 o 3 ,2 ossergni id inoizargufinoc ni ilibinopsid onos ociremmod ni ilibinopsid etaG RON CI ilg e itarapes issergni id isaislaug oremun nu ereva 'Aup ,RO enoizunf al emoc ,RON enoizunf aL ,RON e DNAN ,TON ,RO ,DNA etrop ella ertlo ,RON-xE e RO-xE ,ilaliceps etrop eud onos iC .STEF ,)TEF rotcudnocimeS edixO lateM( TEFsOM ,SOMC aigoloncet al odnazillitu etuirtsoc onos esab id ehcgol etrop el ,acitarp ni ,aM .eud o onu id enoizanimoc anu ni itavort eresse onossop illecnac itseuq ,ertlonletaG ROxetaG TONetaG DNAetaG RO.ottos itacidni onos inuoc illeuQ ,ilanoizatupmoc itiucric id Ateirav anu ni eravort 'Aup is ehc ocinu ollecnac id opit nu 'A ollecnac otseuQ ,orol art etrop etlom odnagelloc otaerc 'A ,airotanimoc acigol emoc ehcna oton ,A-elbaT hturT otanimoc ocigol ametsis lI)everb a atanroigga Arrev enigammi'L( A.olobmis led eralocric etnenopmoc allad otacidni emoc ,tupni' odnagen ats tuptuo'L ,ROX ollecnac li emoc oton 'A RO-evisulocX ollecnac lI A- Atirev allebaT)otserp atanroigga Arrev enigammi'L(-etaG RON id ocigol ammargaiD.'Aip id errudorp rep atacsac ni issem eresse onossop RON illecnac icipit i ,issergni 'Aip irassecen onos eS ,oiranib tuptuo nu ni onatlisur e iranib tupni 'Aip o onu us ehcgol inoizarepo el erugese rep etazzillitu onognev ehcgol etrop eL ,DNAN atrop alled acigol enoizarepo'l noc TON atrop alled olobmis li erateserppar rep tuptuo ous la ,'enoiserevni id allob' emoc otacidni etlov a ,oihrec nu noc elamron DNA atrop anu a ailgimossa ehc olobmis nu onovecir e ataresid acigol enoizunf al erimof rep ilatigid itiucric onazzillitu ehcgol DNAN etrop eL ,etaG TON id ocigol ammargaiD ,otanimoc ocigol ametsis nu id enoizattogorp al etnarud tupni id inoizidnoc 'Aip rep icigol tuptuo eranibba rep 'Atirev id ellebat erazzillitu omaissop A A elaugu Y emoc aticer ehc A,B + A = Y.ad atad eresse 'Aup RO etag led anaeloob enoisserep' L'noos detadpU eb lliw egaml(Noos detadpU eb lliw egaml,ad atad eresse 'Aup RO etag B. The truth of the two-input or basic gate can be supplied as follows. in and gate, the output reaches state 1 if and only if all the inputs are in state 1, (the image will soon be updated ) (Image will be updated early) You can give a boolean expression and the gate can be given by, y = a. An arrow forward with a small circle at the exit is the non-gate. Their boolean production feature is powerful enough to define a complete logic gate. The input and output report is based on a specific logic. And gate, or gate, not gate, and so on are examples of logic gates.a Logic Gate is an idealistic calculation model or a practical electronic device that implements a boolean function, which is a logical operation by A single binary outlet from one or more binary inputs.Types of Logic Gatesit has an output and n input (N> = 2). (The image will soon be updated) This is the logical scheme and the gate. The basic logical doors are classified in seven types as and, or, Xor, NAND, N6, Xnor and not. These are the important digital devices, based mainly on the Boolean function. The logical doors are used in microcontrollers, microprocessors, electronic and electrical circuits of the project and built-in system applications. The entrance side of the gate is curved, while the output side is pointed abruptly. (The image will be updated early) Table of the truth: If and only if the input does not reach the state 1, the output of a gate does not reach the state 1. A specific logic rules the relationship between the input and the Output. The logic nA © The gates are available using the digital circuits to generate the appropriate logical function and a symbol is assigned that resembles a normal gate with a circle, commonly referred to as a "inversion bubble", to its output to indicate Gato's symbol with logical nA© gate operation. These gates are not basic doors in and of themselves. They consist of other logical ports. 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